



Date: 17-05-2023

Event Report

Activity: “Empowering Non-Teaching Staff: A Workshop on System Troubleshooting”

INTRODUCTION/OVERVIEW:

The **Internal Quality Assurance Cell (IQAC)** has organized an “**Empowering Non-Teaching Staff: A Workshop on System Troubleshooting**” on **17-05-2023 at 2:00 PM, Skill Development Lab, B block 1st Floor**. For all the departments instructors of SKIT.

Session started with Prof. Imran Ulla Khan, Member IQAC welcomed all the participants and the **resource person Mr. Prasad, System Admin, SKIT**.

This hands-on training program is designed to provide non-teaching staff with the skills and knowledge they need to troubleshoot common computer system issues. The workshop will cover the basics of computer systems and troubleshooting techniques, and participants will have the opportunity to practice diagnosing and resolving common issues on their own. We will also focus on promoting a culture of self-sufficiency and collaboration within the organization, by encouraging participants to share their knowledge and skills with others

The resource person Mr. Prasad explained the common technical issues raises during the operation of computer system and demonstrated how to resolve them. He also demonstrated how to install operating system through CD and pen drive.

Prof. Imran Ulla Khan whole-heartedly thank Management, Principal and Head IQAC for their support. Thanks to Resource person for sharing his knowledge and expertise with participants. Finally thanks to all participants who took part in this session.

OBJECTIVES:

1. To provide non-teaching staff with a basic understanding of computer systems and troubleshooting techniques, so that they can diagnose and resolve common issues on their own.
2. To increase the confidence and competence of non-teaching staff in dealing with technical problems, so that they feel more empowered to take ownership of their work and support others.
3. To promote a culture of self-sufficiency and collaboration within the organization, by encouraging non-teaching staff to share their knowledge and skills with others.



4. To enhance the efficiency and productivity of the organization, by reducing the dependence on IT support for routine system issues and allowing non-teaching staff to focus on their core responsibilities.

SUMMARY:

"Empowering Non-Teaching Staff: A Workshop on System Troubleshooting" is a hands-on training program designed to provide non-teaching staff with the skills and knowledge they need to troubleshoot common computer system issues. Participants will learn the basics of computer systems and troubleshooting techniques, and will have the opportunity to practice diagnosing and resolving common issues on their own. The workshop will also focus on promoting a culture of self-sufficiency and collaboration within the organization, by encouraging participants to share their knowledge and skills with others. By the end of the workshop, participants will have improved technical skills, increased confidence and competence, and a better understanding of how to enhance the efficiency and productivity of their organization

OUTCOMES:

1. Improved technical skills: Participants will gain a better understanding of computer systems and troubleshooting techniques, and will be able to diagnose and resolve common issues on their own.
2. Increased confidence and competence: Participants will feel more empowered to take ownership of their work and support others in their organization, which can lead to greater job satisfaction and productivity.
3. Improved communication and collaboration: Participants will have the opportunity to share their knowledge and skills with others, which can promote a culture of self-sufficiency and collaboration within the organization.
4. Greater efficiency and productivity: By reducing the dependence on IT support for routine system issues, non-teaching staff will be able to focus more on their core responsibilities, which can enhance the efficiency and productivity of the organization

Event Coordinator
Prof. Imran Ulla Khan
Member, IQAC
Dept. CSE, SKIT
Bangalore-90

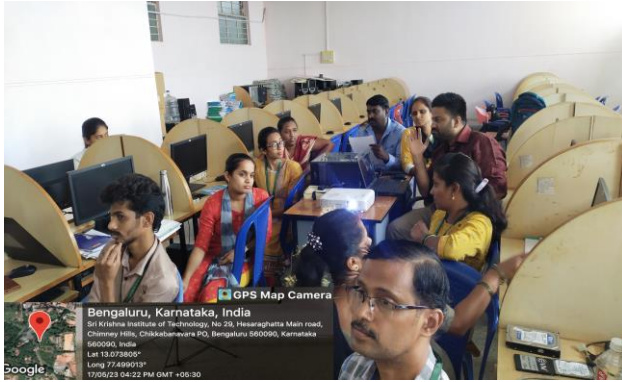
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SCREENSHOTS/PICTURES:



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Event Conduction Approval

Event Coordinator name: Mrs. Sowmya C V and Aruna R

Email: sowmyaevase@skit.org.in

Contact no: 7406750518

Department: Computer Science and Engineering

Event name: Workshop on Computer Organization and Architecture

Resource Person/s: Dr. K. Badari Nath

Date of conduction of the event: 06/01/2023 and 07/01/2023

Type of Event (Technical/Extracurricular/Others (be specific)): Technical

Event Description:

The Computer Architecture helps us to understand the functionalities of a system. Computer Organization tells us how exactly all the units in the system are arranged and interconnected. A programmer can view architecture in terms of instructions, addressing modes and registers.

Computer architectures represent the means of interconnectivity for a computer's hardware components as well as the mode of data transfer and processing exhibited. Different computer architecture configurations have been developed to speed up the movement of data, allowing for increased data processing.

From computer science and engg department we would like conduct a hands-on session on designing of computer organization and architecture for 2 days (6-7th).

We start from basics and end the workshop with one assignment where they will learn to design one CPU structure by simulation tool.

Outcome of the Event or Activity:

Upon completion of this workshop the student is able to:

- Design a CPU from scratch.
- Understanding of 2's complement to represent negative numbers in binary.
- Creating a complete ALU using transistors.
- Understanding of control unit and why we need it.
- Describing how to create devices for processing data.



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Workshop Breakdown

Session Description:

Day-1:

- Logisim software tools – libraries, creation, simulation, designing of storage system.
- ALU design.
- Register file design.
- PC design.
- Program memory design.
- Instruction register, decoding and control unit.

Day-2:

- Instruction design and machine code generation.
- Linking all the modules with multi bus architecture and execution of simple program with Mov and Add instructions.

Requirements:

Laptop & internet as it will be a hands-on session software requirements – Simulation software for designing of CPU certificate will be provided.

Estimated attendance: 57 students from CSE

Venue of the event: CSE ICT Class Room

Estimated cost for the event: 200 per student.

Saonara

Event Coordinator

[Signature]
04/01/2023
Head of the Department

Savita
04/01/2023
IQAC Head

[Signature]
04/01/2023
Principal



Event Report

Activity : Workshop on Computer Organization and Architecture Development

INTRODUCTION/OVERVIEW:

{Event name: Workshop on Computer Organization and Architecture Development,

Resource person: Dr. K Badari Nath,

Date of conduction: 06/01/2023 and 07/01/2023,

Number of participants: 57 students from CSE Platform used – CSE ICT classroom }

Event Description:

The Computer Architecture helps us to understand the functionalities of a system. Computer Organization tells us how exactly all the units in the system are arranged and interconnected. A programmer can view architecture in terms of instructions, addressing modes and registers.

Computer architectures represent the means of interconnectivity for a computer's hardware components as well as the mode of data transfer and processing exhibited. Different computer architecture configurations have been developed to speed up the movement of data, allowing for increased data processing.

From computer science and engg department we would like conduct a hands-on session on designing of computer organization and architecture for 2 days (6-7th).

We start from basics and end the workshop with one assignment where they will learn to design one CPU structure by simulation tool.

Prerequisites:

Before understanding of computer organization the students must have knowledge of Basic Logic gates, functional components of computer. Etc.

OBJECTIVES:

The objective of the workshop is:

- The purpose of the course is to introduce principles of computer organization and the basic architectural concepts.
- Its main objective is to make us understand the over-all computer hardware structure and all its peripheral devices



Internal Quality Assurance Cell (IQAC)

- It begins with basic organization, design, and programming of a simple digital computer and introduces simple register transfer language to specify various computer operations.

SUMMARY:

The main aim of this workshop is to enabling the student with basic knowledge on the techniques to build a CPU with high speed processor. This course covers the techniques on how to design a model, how it can be evaluated, what are all different requirements and components they can adopt to make efficient CPU model.

OUTCOMES:

Upon completion of this workshop the student is able to:

- Design a CPU from scratch.
- Understanding of 2's complement to represent negative numbers in binary.
- Creating a complete ALU using transistors.
- Understanding of control unit and why we need it.
- Describing how to create devices for processing data.

Topics covered in Workshop:

Session Description:

Day-1:

- Logisim software tools – libraries, creation, simulation, designing of storage system.
- ALU design.
- Register file design.
- PC design.
- Program memory design.
- Instruction register, decoding and control unit.

Day-2:

- Instruction design and machine code generation.
- Linking all the modules with multi bus architecture and execution of simple program with Mov and Add instructions.



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SCREENSHOTS/PICTURES:





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Flyer:



Sri Saghavendra Educational Institutions Society
Sri Krishna Institute of Technology
(Approved by AICTE, Affiliated to VTU, Karnataka)

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
Is Organising workshop on
Computer Organisation and Architectural Development

Resource person:

Dr.K Badari Nath
Associate professor
CSE Department
RVCE, Bangalore -560069

organise

Computer Science and
Engineering Department
SKIT

Objective:

- To learn the hardware components
- Understand of different circuits
- Developing CPU by simulation tool

Co-ordinator:

Professor Sowmya CV
Professor Aruna R
CSE Department SKIT
Phone: 7406750518

Date :06/01/23 & 07/01/23

Time: 9:00am to 4:00 pm

Venue: CSE Department



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Internal Quality Assurance Cell (IQAC)

Event Conduction Approval

*Application must be submitted two weeks prior to the date of event

Event Coordinator name: Mrs. Kavya M and Mrs. Rashmi K T

Email: kavyamcse@skit.org.in, rashmicse@skit.org.in Contact no: 8618008931, 9886116509

Department: Computer Science and Engineering

Event name: **Workshop [SDP] on Cyber Security and Ethical Hacking**

Resource Person/'s: Mr. Sohan Simha, Trainer in Ethical Byte

Date of conduction of the event: 08/02/2023, 09/02/2023, 10/02/2023, 11/02/2023

Type of Event (Technical/Extracurricular/Others (be specific)): Technical

Event Description: Cyber security 2022 uniquely addresses cyber security, cyber warfare and information warfare. For the past 17 years cyber security has developed into an important in the cyber-security field, attracting academics, military professionals and practitioners from around the world to present their research findings in the form of empirical studies, case histories and other theoretical and practical contributions.

With members from around the world focused on learning about Cyber Security and Ethical Hacking this is your single best opportunity to reach the largest assemblage of registered participant students from 3rd sem of CSE & ISE, Sri Krishna Institute of Technology, to conduct demonstrations, to distribute information, to acquire knowledge about current and trending global technologies, to make a splash with a new research, and to receive name recognition. World-renowned speakers, the most recent techniques, tactics, and the newest updates in the CYBER SECURITY are the hallmarks of this SDP.

Outcome of the Event or Activity:

Upon completion of this SDP the Students is expected to:

The participants found the event interactive and effective. They understood how to process massive amounts of data that are available both in structured and unstructured forms, and how to analyze those data in an iterative, as well as in a time-sensitive manner. The participants also gained awareness on how to protect their personal system, mobile and data from cyber-attacks.

Workshop Breakdown

Session Description:



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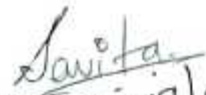
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1. Introduction to Ethical Hacking
2. Foot printing and Reconnaissance 03: Scanning Networks
3. Enumeration
4. Vulnerability Analysis
5. System Hacking
6. Malware Threats
7. Sniffing
8. Social Engineering
9. Denial-of-Service
10. Session Hijacking
11. Evading IDS and Honeypots
12. Hacking Web Servers
13. Hacking Web Applications
14. SQL Injection
15. Hacking Wireless Networks
16. Hacking Mobile Platforms
17. IOT Hacking
18. Cryptography

Estimated attendance: All 4th SEM Students CSE, SKIT.

Venue of the event: 202 ICT Room, CSE


Event Coordinator


IQAC Head 07/02/2023.


Head of the Department
Computer Science & M. Engg.
Sri Krishna Institute of Technology

Principal 07/02/23

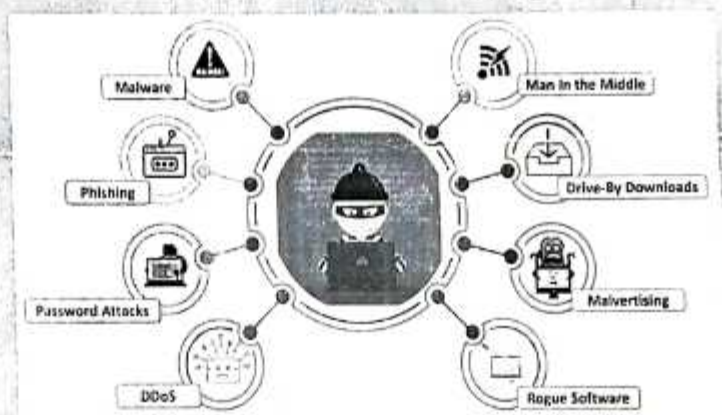


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DEPARTMENT OF
COMPUTER SCIENCE & ENGINEERING

Organizing an SDP on
Cyber Security
&
Ethical Hacking



Resource Person
Mr. Sohan Simha
Trainer in Ethical
Byte

Date: February 8th to 11th, 2023

Time: 09.30AM to 03.30PM

Venue: CSE Department, SKIT

Register your Participation with Coordinator

Co-ordinator
Prof. Kavya M
Asst. Professor, CSE

HOD
Dr. Shanfaram Nayak
SKIT, CSE

Principal
Dr. Mahesha K
SKIT, Bengaluru



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INTERNAL QUALITY ASSURANCE CELL (IQAC)

Date: 13/02/2023

Event Report

Activity: SDP on Cyber Security and Ethical Hacking (VAC)

INTRODUCTION/OVERVIEW:

Event Name: SDP on Cyber Security and Ethical Hacking (VALUE ADDED COURSE)

Date: 07/02/2023, 08/02/2023, 09/02/2023, 10/02/2023, 11/02/2023

Guest of Honor: Mr.Sohan Simha, Training Officer–Ethical Byte.

No. of Students Participants: Registered 3rd Sem Students

Duration: 7 and half-hours (9.00am to 5.00pm) 3 days complete session & 4 hours on 7-2-2023 for installation and 4 hours on 11-02-2023 for revision and certification text.

OBJECTIVES:

1. Vision and Mission of **Ethical Byte** foundation.
2. Motivation towards Cyber Attacks.

SUMMARY:

Department of Computer Science & Engineering in association with IQAC-SKIT, organized a **Student development program** from **07-02-2023 to 11-02-2023** in OFFLINE mode. The topic of program is :“ SDP on Cyber Security and Ethical Hacking level-1”, Mr. Sohan Simha is Technical corporate trainer for Ethical Hacking, Computer forensic and IOT, Embedded system Networking Ethical Byte has been a long-term trusted partner for clients, working closely with our clients allows us to act in their best interest over the long term. The aim of the event is to inspire and encourage Students to get the information on cyber-attacks with session offline mode. Prof. Kavya M started the event by welcoming participates and Speaker. Dr.Shantharam Nayak greeted the speaker and participants. He shared a very good knowledge about upcoming cyber-attacks with hands on session. After this session we collected the feedback from students for Course on Cyber Security and Ethical Hacking level 1. In this document we got 95% interest rate for level 1 is good and students are inserted to take level 2 cyber security course also. The session was very interactive. Prof. Kavya M and Prof. Rashmi K T has coordinated this event.

OUTCOMES:

1. Covey the importance of knowledge on Cyber-attacks.
2. Motivate students towards gaining the knowledge.
3. Motivation regarding Cyber Security Course level 2.

Feedback link address: <http://feedback.ethicalbyte.in/>

**DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
IN ASSOCIATION WITH IQAC AND PLACEMENT OF SKIT**

Report on Value added course (SDP)

"Cyber Security and Ethical Hacking"

Detailed Syllabus

07-02-2023 (Tuesday)	Session After-noon 1.00pm-5.00pm	Installation of Kali Linux and parrot documentation software's for students laptops.
08-02-2023 (Wednesday)	Session-1 9.00am-1.00 pm	Introduction to ethical hacking, foot printing and reconnaissance 03
08-02-2023 (Wednesday)	Session-2 1.30 -5.00pm	Enumeration, vulnerability analysis, system hacking, malware threats, sniffing
09-02-2023 (Thursday)	Session-1 9.00am-1.00 pm Session-2 1.30 -5.00pm	Social engineering, denial of service, session hijacking, evading IDS and Honeypots, hacking web servers.
10-02-2023 (Thursday)	Session-1 9.00am-1.00 pm Session-2 1.30 -5.00pm	Hacking web applications, SQL injection, hacking wireless networks, hacking mobile platforms, IOT hacking, cryptography.
11-02-2023 (Saturday)	Session-1 9.00am-1.00 pm	MOCK test and revision session. Certification exam on cyber security level 1

SCREENSHOTS/PICTURES:



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CIRTIIFICATE



CERTIFICATE OF ACHIEVEMENT

THIS IS TO RECOGNIZE

Varshitha.S

Has been awarded this certificate for successfully completing the course
CYBER SECURITY LEVEL 1

EBCSL1025K230004
CERTIFICATE NUMBER



Sunoj Kashyap
SUNOJ KASHYAP
DIRECTOR



verify - <https://ethicalbyte.in/verify.php>

DATE: - 11/02/2023

ATTENDED STUDENTS LIST

SL NO	NAME
1	Varshitha.S
2	Mohamed Wajahat
3	Rowin Fernandes
4	Jayanth N B
5	AKSHITHA K
6	Meghana S M
7	Vaishnavi.B
8	VISHNU YADAV M N
9	Suhas s pai
10	Meghana M
11	Gayathri.H
12	Sushmitha.V
13	Divya. K
14	Tejashwini.S



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INTERNAL QUALITY ASSURANCE CELL (IQAC)

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16	Poornima.GS
17	Nithya K
18	Harsha vardhan reddy
19	Samiksha
20	Gnanashree G
21	Hitesh S
22	Harshitha R
23	HARSHITHA K P
24	SANKEERTH BD
25	Shankar KB
26	Rakshitha N
27	Thanmayee B S
28	Neha .T
29	Meghana HR
30	Harshitha B
31	Shravya K R
32	Harshita nataha
33	Harish s.k
34	Sneha. M
35	UDAY B
36	RENUSHREE.G
37	Sinchana R Gonimath
38	Sonal Kumari
39	Ardra Hariprasad
40	Abijith Barath C
41	Vishwas Patel
42	Harshitha V
43	Soundraya

P. 14/02/23 Ray
17/2/23
Event Coordinator/s


HOD
Head of the Department
Computer Science & Engg.
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Internal Quality Assurance Cell (IQAC)

Event Conduction Approval

**Application must be submitted two weeks prior to the date of event*

Event Coordinator name: Mrs. Sowmya C V

Email: sowmyacvcese@skit.org.in

Contact no: 7406750518

Department: Computer Science and Engineering

Event name: Workshop on Machine Learning

Resource Person's: Ms. Megha

Date of conduction of the event: 07/10/2022

Type of Event (Technical/Extracurricular/Others (be specific)): Technical

Event Description:

The main objective of this course is to enabling the student with basic knowledge on the techniques to build an intellectual machine for making decisions behalf of humans. This course covers the techniques on how to make learning by a model, how it can be evaluated, what are all different algorithms to construct a learning model.

As we know machine learning can be considered as a most trending technology in present and as well as in future it's very important to get exposure.

From Mevi technologies LLP we would like conduct a hands-on machine learning workshop for a day (7-8 hours).

We start from basics and end the workshop with one project where they might take that project to advance level like final year project.

Outcome of the Event or Activity:

Upon completion of this workshop the student is able to:

- Develop an appreciation for what is involved in Learning models from data.
- Understand a wide variety of learning algorithms.
- Understand how to evaluate models generated from data.
- Apply the algorithms to a real problem. optimize the models learned and report on the expected accuracy that can be achieved by applying the models.



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Workshop Breakdown

Session Description:

- Introduction to python data structures (45 min)
- scikit-learn
- numpy
- pandas
- intro to machine learning
- types of machine learning
- exploring the data and visualizing the data using matplotlib and seaborn
- types of data and dataset
- training and testing data
- confusion matrix
- introduction to machine learning algorithms
- linear regression and logistic regression
- 2+1 projects
- Intro to open cv(bonus lecture)

Requirements:

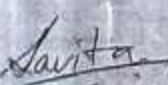
Laptop & internet as it will be a hands-on session software requirements - anaconda with python 3.x e- certificate will be provided.

Estimated attendance: 80 students from CSE and ISE

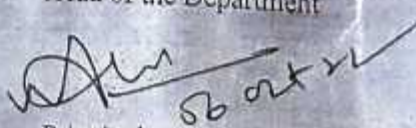
Venue of the event: CSE ICT Class Room

Estimated cost for the event: 4000


Event Coordinator 06/10/22


IQAC Head 06/10/22


Head of the Department 06/10/2022


Principal 06/10/22



Event Report

Activity : Workshop on Machine Learning

INTRODUCTION/OVERVIEW:

{Event name: Workshop on Machine Learning, Resource person: Ms. Megha B S, Date of conduction: 07/10/2022, Number of participants: 74 students from CSE and ISE and Platform used – CSE ICT classroom }

Event Description:

Machine Learning is one of the most popular sub-fields of Artificial Intelligence. Machine learning concepts are used almost everywhere, such as Healthcare, Finance, Infrastructure, Marketing, Self-driving cars, recommendation systems, chatbots, social sites, gaming, cyber security, and many more. Currently, Machine Learning is under the development phase, and many new technologies are continuously being added to Machine Learning. It helps us in many ways, such as analyzing large chunks of data, data extractions, interpretations, etc. Hence, there are unlimited numbers of uses of Machine Learning.

The machine learning field is continuously evolving. And along with evolution comes a rise in demand and importance. There is one crucial reason why data scientists need machine learning, and that is: 'High-value predictions that can guide better decisions and smart actions in real-time without human intervention.

As we know machine learning can be considered as a most trending technology in present and as well as in future it's very important to get exposure.

From Mevi technologies LLP we would like conduct a hands-on machine learning workshop for a day (7-8 hours).

We start from basics and end the workshop with one project where they might take that project to advance level like final year project.

Prerequisites:

Machine Learning Crash Course does not presume or require any prior knowledge in machine learning. However, to understand the concepts presented and complete the exercises, we recommend that students meet the following prerequisites:



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- Students must be comfortable with variables, linear equations, graphs of functions, histograms, and statistical means.
- Students should be a good programmer. Ideally, you should have some experience programming in Python because the programming exercises are in Python. However, experienced programmers without Python experience can usually complete the programming exercises anyway.

OBJECTIVES:

The objective of the workshop is:

- To understand the basic theory underlying machine learning.
- To be able to formulate machine learning problems corresponding to different applications.
- To understand a range of machine learning algorithms along with their strengths and weaknesses. To be able to apply machine learning algorithms to solve problems of moderate complexity.
- To apply the algorithms to a real-world problem, optimize the models learned and report on the expected accuracy that can be achieved by applying the models.

SUMMARY:

The main aim of this workshop is to enabling the student with basic knowledge on the techniques to build an intellectual machine for making decisions behalf of humans. This course covers the techniques on how to make learning by a model, how it can be evaluated, what are all different algorithms to construct a learning model.

OUTCOMES:

Upon completion of this workshop the student is able to:

- Develop an appreciation for what is involved in Learning models from data.
- Understand a wide variety of learning algorithms.
- Understand how to evaluate models generated from data.
- Apply the algorithms to a real problem, optimize the models learned and report on the expected accuracy that can be achieved by applying the models.

Topics covered in Workshop:



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Introduction to Programming, Python basics, Applications of python, data types, string methods, loops (for and while), conditional statements, 3 to 4 programs in python.

Introduction to machine learning, Types of machine learning, different algorithm in machine learning, data set, Training and testing data sets libraries, numpy and pandas, what are real world applications of machine learning, 1 algorithm linear regression Salary prediction of employee mini project.

SCREENSHOTS/PICTURES:



Flyer:



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Sri Raghavendra Educational Institutions Society
Sri Krishna Institute of Technology
(Approved by AICTE, Affiliated to VTU, Karnataka)



WORKSHOP ON

MACHINE LEARNING



RESOURCE PERSON

**MS.
MEGHA B S**

COMPANY: *MeVi Technologies* DESIGNATION: *Trainer and Developer*

OBJECTIVES:

- Develop an appreciation for what is involved in Learning models from data.
- Understand a wide variety of learning algorithms.
- Understand how to evaluate models generated from data.
- Apply the algorithms to a real problem, optimize the models.

DATE: 07/10/22

TIME: 9:00AM-4:00PM

VENUE: CSE ICT ROOM

CO-ORDINATOR: SOWMYA C.V

PH NO: 7406750518

Certificate:

Sowmya C.V.
Event Coordinator/s
Sowmya C V



Event Conduction Approval

Event Coordinator name: Mrs. Sowmya C V and Aruna R

Email: sowmyaevase@skit.org.in

Contact no: 7406750518

Department: Computer Science and Engineering

Event name: Workshop on Computer Organization and Architecture

Resource Person/s: Dr. K. Badari Nath

Date of conduction of the event: 06/01/2023 and 07/01/2023

Type of Event (Technical/Extracurricular/Others (be specific)): Technical

Event Description:

The Computer Architecture helps us to understand the functionalities of a system. Computer Organization tells us how exactly all the units in the system are arranged and interconnected. A programmer can view architecture in terms of instructions, addressing modes and registers.

Computer architectures represent the means of interconnectivity for a computer's hardware components as well as the mode of data transfer and processing exhibited. Different computer architecture configurations have been developed to speed up the movement of data, allowing for increased data processing.

From computer science and engg department we would like conduct a hands-on session on designing of computer organization and architecture for 2 days (6-7th).

We start from basics and end the workshop with one assignment where they will learn to design one CPU structure by simulation tool.

Outcome of the Event or Activity:

Upon completion of this workshop the student is able to:

- Design a CPU from scratch.
- Understanding of 2's complement to represent negative numbers in binary.
- Creating a complete ALU using transistors.
- Understanding of control unit and why we need it.
- Describing how to create devices for processing data.



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Internal Quality Assurance Cell (IQAC)

Workshop Breakdown

Session Description:

Day-1:

- Logisim software tools – libraries, creation, simulation, designing of storage system.
- ALU design.
- Register file design.
- PC design.
- Program memory design.
- Instruction register, decoding and control unit.

Day-2:

- Instruction design and machine code generation.
- Linking all the modules with multi bus architecture and execution of simple program with Mov and Add instructions.

Requirements:

Laptop & internet as it will be a hands-on session software requirements – Simulation software for designing of CPU certificate will be provided.

Estimated attendance: 57 students from CSE

Venue of the event: CSE ICT Class Room

Estimated cost for the event: 200 per student.

Saonara

Event Coordinator

[Signature]
04/01/2023
Head of the Department

Savita
04/01/2023
IQAC Head

[Signature]
04/01/2023
Principal



Event Report

Activity : Workshop on Computer Organization and Architecture Development

INTRODUCTION/OVERVIEW:

{Event name: Workshop on Computer Organization and Architecture Development,

Resource person: Dr. K Badari Nath,

Date of conduction: 06/01/2023 and 07/01/2023,

Number of participants: 57 students from CSE Platform used – CSE ICT classroom }

Event Description:

The Computer Architecture helps us to understand the functionalities of a system. Computer Organization tells us how exactly all the units in the system are arranged and interconnected. A programmer can view architecture in terms of instructions, addressing modes and registers.

Computer architectures represent the means of interconnectivity for a computer's hardware components as well as the mode of data transfer and processing exhibited. Different computer architecture configurations have been developed to speed up the movement of data, allowing for increased data processing.

From computer science and engg department we would like conduct a hands-on session on designing of computer organization and architecture for 2 days (6-7th).

We start from basics and end the workshop with one assignment where they will learn to design one CPU structure by simulation tool.

Prerequisites:

Before understanding of computer organization the students must have knowledge of Basic Logic gates, functional components of computer. Etc.

OBJECTIVES:

The objective of the workshop is:

- The purpose of the course is to introduce principles of computer organization and the basic architectural concepts.
- Its main objective is to make us understand the over-all computer hardware structure and all its peripheral devices



- It begins with basic organization, design, and programming of a simple digital computer and introduces simple register transfer language to specify various computer operations.

SUMMARY:

The main aim of this workshop is to enabling the student with basic knowledge on the techniques to build a CPU with high speed processor. This course covers the techniques on how to design a model, how it can be evaluated, what are all different requirements and components they can adopt to make efficient CPU model.

OUTCOMES:

Upon completion of this workshop the student is able to:

- Design a CPU from scratch.
- Understanding of 2's complement to represent negative numbers in binary.
- Creating a complete ALU using transistors.
- Understanding of control unit and why we need it.
- Describing how to create devices for processing data.

Topics covered in Workshop:

Session Description:

Day-1:

- Logisim software tools – libraries, creation, simulation, designing of storage system.
- ALU design.
- Register file design.
- PC design.
- Program memory design.
- Instruction register, decoding and control unit.

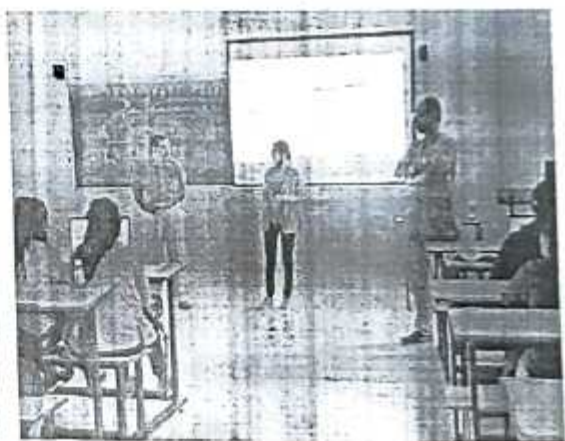
Day-2:

- Instruction design and machine code generation.
- Linking all the modules with multi bus architecture and execution of simple program with Mov and Add instructions.



Internal Quality Assurance Cell (IQAC)

SCREENSHOTS/PICTURES:





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Internal Quality Assurance Cell (IQAC)

Flyer:



Sri Saghavendra Educational Institutions Society
Sri Krishna Institute of Technology
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DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
Is Organising workshop on
Computer Organisation and Architectural Development

Resource person:

Dr.K Badari Nath
Associate professor
CSE Department
RVCE, Bangalore -560069

Organise:

Computer Science and
Engineering Department
SKIT

Objective:

- To learn the hardware components
- Understand of different circuits
- Developing CPU by simulation tool

Co-ordinator:

Professor Sowmya CV
Professor Aruna R
CSE Department SKIT
Phone: 7406750518

Date :06/01/23 & 07/01/23

Time: 9:00am to 4:00 pm

Venue: CSE Department



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Internal Quality Assurance Cell (IQAC)

Event Conduction Approval

*Application must be submitted two weeks prior to the date of event

Event Coordinator name: Mrs. Kavya M and Mrs. Rashmi K T

Email: kavyamcse@skit.org.in, rashmicse@skit.org.in Contact no: 8618008931, 9886116509

Department: Computer Science and Engineering

Event name: **Workshop [SDP] on Cyber Security and Ethical Hacking**

Resource Person/'s: Mr. Sohan Simha, Trainer in Ethical Byte

Date of conduction of the event: 08/02/2023, 09/02/2023, 10/02/2023, 11/02/2023

Type of Event (Technical/Extracurricular/Others (be specific)): Technical

Event Description: Cyber security 2022 uniquely addresses cyber security, cyber warfare and information warfare. For the past 17 years cyber security has developed into an important in the cyber-security field, attracting academics, military professionals and practitioners from around the world to present their research findings in the form of empirical studies, case histories and other theoretical and practical contributions.

With members from around the world focused on learning about Cyber Security and Ethical Hacking this is your single best opportunity to reach the largest assemblage of registered participant students from 3rd sem of CSE & ISE, Sri Krishna Institute of Technology, to conduct demonstrations, to distribute information, to acquire knowledge about current and trending global technologies, to make a splash with a new research, and to receive name recognition. World-renowned speakers, the most recent techniques, tactics, and the newest updates in the CYBER SECURITY are the hallmarks of this SDP.

Outcome of the Event or Activity:

Upon completion of this SDP the Students is expected to:

The participants found the event interactive and effective. They understood how to process massive amounts of data that are available both in structured and unstructured forms, and how to analyze those data in an iterative, as well as in a time-sensitive manner. The participants also gained awareness on how to protect their personal system, mobile and data from cyber-attacks.

Workshop Breakdown

Session Description:



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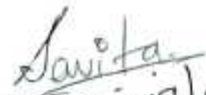
Internal Quality Assurance Cell (IQAC)

1. Introduction to Ethical Hacking
2. Foot printing and Reconnaissance 03: Scanning Networks
3. Enumeration
4. Vulnerability Analysis
5. System Hacking
6. Malware Threats
7. Sniffing
8. Social Engineering
9. Denial-of-Service
10. Session Hijacking
11. Evading IDS and Honeypots
12. Hacking Web Servers
13. Hacking Web Applications
14. SQL Injection
15. Hacking Wireless Networks
16. Hacking Mobile Platforms
17. IOT Hacking
18. Cryptography

Estimated attendance: All 4th SEM Students CSE, SKIT.

Venue of the event: 202 ICT Room, CSE


Event Coordinator


IQAC Head 07/02/2023.


Head of the Department
Computer Science & N. Engg.
Sri Krishna Institute of Technology

Principal 07/02/23

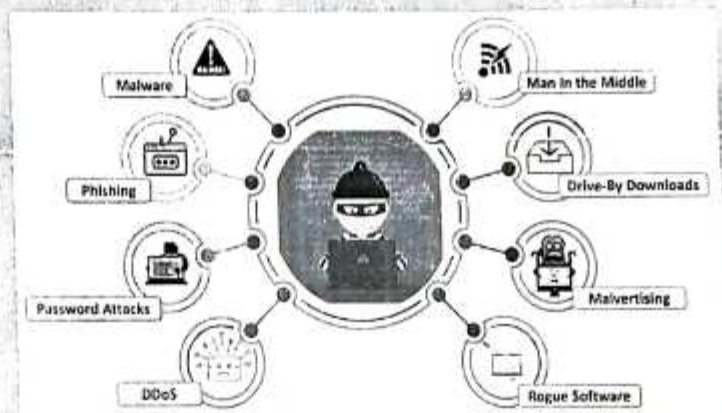


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DEPARTMENT OF
COMPUTER SCIENCE & ENGINEERING

Organizing an SDP on
Cyber Security
&
Ethical Hacking



Resource Person
Mr. Sohan Simha
Trainer in Ethical
Byte

Date: February 8th to 11th, 2023

Time: 09.30AM to 03.30PM

Venue: CSE Department, SKIT

Register your Participation with Coordinator

Co-ordinator
Prof. Kavya M
Asst. Professor, CSE

HOD
Dr. Shanfaram Nayak
SKIT, CSE

Principal
Dr. Mahesha K
SKIT, Bengaluru



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INTERNAL QUALITY ASSURANCE CELL (IQAC)

Date: 13/02/2023

Event Report

Activity: SDP on Cyber Security and Ethical Hacking (VAC)

INTRODUCTION/OVERVIEW:

Event Name: SDP on Cyber Security and Ethical Hacking (VALUE ADDED COURSE)

Date: 07/02/2023, 08/02/2023, 09/02/2023, 10/02/2023, 11/02/2023

Guest of Honor: Mr.Sohan Simha, Training Officer–Ethical Byte.

No. of Students Participants: Registered 3rd Sem Students

Duration: 7 and half-hours (9.00am to 5.00pm) 3 days complete session & 4 hours on 7-2-2023 for installation and 4 hours on 11-02-2023 for revision and certification text.

OBJECTIVES:

1. Vision and Mission of **Ethical Byte** foundation.
2. Motivation towards Cyber Attacks.

SUMMARY:

Department of Computer Science & Engineering in association with IQAC-SKIT, organized a **Student development program** from **07-02-2023 to 11-02-2023** in OFFLINE mode. The topic of program is :“ SDP on Cyber Security and Ethical Hacking level-1”, Mr. Sohan Simha is Technical corporate trainer for Ethical Hacking, Computer forensic and IOT, Embedded system Networking Ethical Byte has been a long-term trusted partner for clients, working closely with our clients allows us to act in their best interest over the long term. The aim of the event is to inspire and encourage Students to get the information on cyber-attacks with session offline mode. Prof. Kavya M started the event by welcoming participates and Speaker. Dr.Shantharam Nayak greeted the speaker and participants. He shared a very good knowledge about upcoming cyber-attacks with hands on session. After this session we collected the feedback from students for Course on Cyber Security and Ethical Hacking level 1. In this document we got 95% interest rate for level 1 is good and students are inserted to take level 2 cyber security course also. The session was very interactive. Prof. Kavya M and Prof. Rashmi K T has coordinated this event.

OUTCOMES:

1. Covey the importance of knowledge on Cyber-attacks.
2. Motivate students towards gaining the knowledge.
3. Motivation regarding Cyber Security Course level 2.

Feedback link address: <http://feedback.ethicalbyte.in/>

**DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
IN ASSOCIATION WITH IQAC AND PLACEMENT OF SKIT**

Report on Value added course (SDP)

"Cyber Security and Ethical Hacking"

Detailed Syllabus

07-02-2023 (Tuesday)	Session After-noon 1.00pm-5.00pm	Installation of Kali Linux and parrot documentation software's for students laptops.
08-02-2023 (Wednesday)	Session-1 9.00am-1.00 pm	Introduction to ethical hacking, foot printing and reconnaissance 03
08-02-2023 (Wednesday)	Session-2 1.30 -5.00pm	Enumeration, vulnerability analysis, system hacking, malware threats, sniffing
09-02-2023 (Thursday)	Session-1 9.00am-1.00 pm Session-2 1.30 -5.00pm	Social engineering, denial of service, session hijacking, evading IDS and Honeypots, hacking web servers.
10-02-2023 (Thursday)	Session-1 9.00am-1.00 pm Session-2 1.30 -5.00pm	Hacking web applications, SQL injection, hacking wireless networks, hacking mobile platforms, IOT hacking, cryptography.
11-02-2023 (Saturday)	Session-1 9.00am-1.00 pm	MOCK test and revision session. Certification exam on cyber security level 1

SCREENSHOTS/PICTURES:



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CIRTIIFICATE



CERTIFICATE OF ACHIEVEMENT

THIS IS TO RECOGNIZE

Varshitha.S

Has been awarded this certificate for successfully completing the course
CYBER SECURITY LEVEL 1



EBCSL1025K230004
 CERTIFICATE NUMBER



Sunoj Kashyap
 SUNOJ KASHYAP
 DIRECTOR

verify - <https://ethicalbyte.in/verify.php>

DATE: - 11/02/2023

ATTENDED STUDENTS LIST

SL NO	NAME
1	Varshitha.S
2	Mohamed Wajahat
3	Rowin Fernandes
4	Jayanth N B
5	AKSHITHA K
6	Meghana S M
7	Vaishnavi.B
8	VISHNU YADAV M N
9	Suhas s pai
10	Meghana M
11	Gayathri.H
12	Sushmitha.V
13	Divya. K
14	Tejashwini.S



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15	POOJA.C
16	Poornima.GS
17	Nithya K
18	Harsha vardhan reddy
19	Samiksha
20	Gnanashree G
21	Hitesh S
22	Harshitha R
23	HARSHITHA K P
24	SANKEERTH BD
25	Shankar KB
26	Rakshitha N
27	Thanmayee B S
28	Neha .T
29	Meghana HR
30	Harshitha B
31	Shravya K R
32	Harshita nataha
33	Harish s.k
34	Sneha. M
35	UDAY B
36	RENUSHREE.G
37	Sinchana R Gonimath
38	Sonal Kumari
39	Ardra Hariprasad
40	Abijith Barath C
41	Vishwas Patel
42	Harshitha V
43	Soundraya

P. 14/02/23 Ray
17/2/23
Event Coordinator/s


HOD
Head of the Department
Computer Science & Engg.
Sri Krishna Institute of Technology
Bangalore-560 090



Sl no	Name	Phone	Email	8/7/2023	9/2/2023	10/2/2023	11/2/2023
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17	Nithya K	8217256507	nithyakowadga2@gmail.com	Nithya K	Nithya K	Nithya K	Nithya K
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19	Samisha	8077711668	samisha.cse@skit.org.in	Samisha	Samisha	Samisha	Samisha
20	Gnanasree	9553621771	gnanasree74@gmail.com	Gnanasree	Gnanasree	Gnanasree	Gnanasree
21	Hireeth S	7699931407	hireethsada31807@gmail.com	Hireeth S	Hireeth S	Hireeth S	Hireeth S
22	HARSHITHA	9886563905	harshitha988@gmail.com	HARSHITHA	HARSHITHA	HARSHITHA	HARSHITHA
23	HARSHITHA	8660758393	harshithakps76@gmail.com	HARSHITHA	HARSHITHA	HARSHITHA	HARSHITHA
24	SANVEER	8861125614	sanveerhbd@gmail.com	SANVEER	SANVEER	SANVEER	SANVEER
25	Shankar K	79754650540	shankarb.cse@skit.org.in	Shankar K	Shankar K	Shankar K	Shankar K
26	Rakshita	18660625395	rahithan.cse@skit.org.in	Rakshita	Rakshita	Rakshita	Rakshita
27	Thaimayee	7483492419	thaimayee.cse@skit.org.in	Thaimayee	Thaimayee	Thaimayee	Thaimayee
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30	Harshitha	9014777306	harshithab.cse@skit.org.in	Harshitha	Harshitha	Harshitha	Harshitha
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32	Harshitha	9666792231	harshithamahata.cse@skit.org.in	Harshitha	Harshitha	Harshitha	Harshitha
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34	Sneha M	7406750581	murthyaj1978@gmail.com	Sneha M	Sneha M	Sneha M	Sneha M
35	UDAY B	8088935982	udayb.cse@skit.org.in	UDAY B	UDAY B	UDAY B	UDAY B
36	REUSHRE	6362721636	reushree556@gmail.com	REUSHRE	REUSHRE	REUSHRE	REUSHRE
37	Sinchara	9900456876	sincharagopinath@gmail.com	Sinchara	Sinchara	Sinchara	Sinchara
38	Sonal Kum	9351818218	sonalkumari.cse@skit.org.in	Sonal Kum	Sonal Kum	Sonal Kum	Sonal Kum
39	Arora Har	8217507744	andharhariprained.cse@skit.org.in	Arora Har	Arora Har	Arora Har	Arora Har
40	Abilim Bar	7019140411	abilimbarath.cse@skit.org.in	Abilim Bar	Abilim Bar	Abilim Bar	Abilim Bar
41	Vishwas Pa	8866806760	vishwaspa23241@gmail.com	Vishwas Pa	Vishwas Pa	Vishwas Pa	Vishwas Pa
42	Abhishek	8192653074	abhisheksk12@gmail.com	Abhishek	Abhishek	Abhishek	Abhishek
43	Sourav	88418833	souravraj.cse@skit.org.in	Sourav	Sourav	Sourav	Sourav
44							
45							
46							

Date: 06/06/2023

Event Report

Activity: 6th National Conference on "Advance and Recent Trends in Electronics and Communication Engineering"

The Department of ECE of Sri Krishna Institute of Technology organized 6th National Conference on "Advance and Recent Trends in Electronics and Communication Engineering" on 02nd June 2023 at 10:00 AM.

The detailed report of the event is given below.

Objectives: The main objective of National Conference on Advanced and Recent trends in Electronics and Communication Engineering (NCAREC-23) is aimed at providing a common platform to Academicians, Research Scholars, Practicing Engineers and Industry Experts to share their innovative ideas, thoughts, findings etc and to discuss so as to ignite the young engineering minds to drive them towards the excellence in Electronics and Communication Engineering and Technology.

Event name: 6th National Conference on "Advance and recent trends in Electronics and Communication Engineering"

Reviewer: Prof. Jaya Lakshmi J

Date of conduction: 02/06/20223

Number of participants: 50

Number of papers: 7 [2 offline and 5 online]

Platform used: ICT Classroom

SUMMARY:

The National Conference on Advance and Recent Trends in Electronics and Communication NCAREC-2023 was organized and inaugurated at 10 AM, for the Students, Faculties of ECE Department the event was started with invocation song and HOD of ECE delivered about NCAREC-23 Conference.

The main attraction of the conference is paper presentation from authors representing host institution, other institution from the state of Karnataka and outside of the state. We received good response for paper submission for the potential participants. The paper submitted have been properly scrutinized by



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

selection panel and possible recommendations were indicated for presentation. Based on the set of papers they have been broadly categorized into two tracks and it has been conducted in both offline & online modes. They are Communication, VLSI & Embedded Systems.

LEARNING OUTCOMES:

To Discuss The Topic Related To Mobile Communication And The Wireless Network, RFID And The Microwave Communication, VLSI Designs, Signal And Image Processing, The Internet Of Things, Artificial Intelligence, RTOS And Neural Networks, Data Mining And The Data Warehousing, Embedded System Fault Tolerant And Cryptography, Sensor Array And Multichannel Signal Processing, Bio- Imaging And Biomedical Signal Processing, Signal Processing For Communication And Networking, Image, Video And Multidimensional Signal Processing, Signal Processing For Big Data, Information Forensics And Security.

SCREENSHOTS/PICTURES:



NCAREC-2023



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NCAREC-2023



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

NCAREC Poster:

Sri Raghavendra Educational Institutions Society[®]



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Department of Electronics & Communication Engineering,

in association with

Institution of Electronics & Telecommunication Engineers

Organizes

6th National Conference

Advanced & Recent Trends in Electronics & Communication Engineering

(NCAREC-23)

2nd
June
2023

Call for paper

Authors are invited to submit full length paper. For submission guidelines refer www.skit.org.in

Important Dates

1. Last date for paper submission: 27-05-2023
2. Intimation of acceptance: 29-05-2023
3. Last Date for registration: 30-05-2023

Fee details

Rs. 600/- per paper - UG / PG Students
Rs. 800/- per paper - Academicians / Research Scholars
Rs. 1000/- per paper - Industry Professionals
Rs. 200/- per additional Author

About the conference

National Conference on Advanced and Recent trends in Electronics and Communication Engineering (NCAREC-23) is aimed at providing a common platform to Academicians, Research Scholars, Practicing Engineers and Industry Experts to share their innovative ideas, thoughts, findings etc and to discuss so as to ignite the young engineering minds to drive them towards the excellence in Electronics and Communication Engineering and Technology.

Paper Submission details:

Authors should submit their manuscripts to Email ID : ncarec23@skit.org.in

Conference Venue

At college auditorium, "SRI KRISHNA INSTITUTE OF TECHNOLOGY"

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Dr. J. Divya Lakshmi
Prof, Dept ECE, SKIT, Bengaluru
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NCAREC-Banner:



NATIONAL CONFERENCE ON ADVANCED & RECENT TRENDS IN ELECTRONICS & COMMUNICATION ENGINEERING

(NCAREC -2023) ON 2nd June 2023 for BE /B.Tech & Research Scholars

HAAC Accredited

PAPERS INVITED FROM EC/COMBINED JOBS

- | | |
|--|--|
| • Field of AI/ML Applications | • Image Processing Applications |
| • Power Electronics | • New Materials |
| • Control Systems | • Embedded and Applications of Smart Systems |
| • VLSI Design | • Robotics |
| • Real Time Embedded Systems | • Nanotechnology |
| • EMI & RFI/EC System Analysis | • Wireless Communication |
| • Wireless Communication | • Microstructure of Air-Crafts |
| • Robotics | • Microfluidics |
| • IoT | |
| • Multi-robot systems (MRS) and multi-robot (MRL) and industrial robot | |
| • Management of Embedded systems | |
| • Paper must be in PDF format and should not exceed 4 pages | |
| • Last date for submission: 27 th May 2023, and to Ready Paper: 29 th May 2023 | |
| • Soft copy of the paper should be sent to: ncarec@skit.ac.in | |

For More Information Contact

Dr. J. Divya Lakshmi (Mob: 9846063664)
divyalakshmi@skit.ac.in
SPALMENT (CO-ORDINATORS)
 Tarun (Mob: 808106874)
 Chaitanya (Mob: 8762133224)

Organized By:

Dept. of Electronics and Communication Engineering
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NCAREC Certificate:



J. Divya
Dr. J Divya Lakshmi
 Co-Convener
 Prof, Dept. of ECE
 SKIT, Bengaluru

N
NAVEEN KUMAR S/Nagaswaja
 Event Coordinator
 Asst. Prof, Dept. of ECE
 SKIT, Bengaluru

C. Patil
Dr. Nagannagouda C Patil
 Convener
 Prof, Dept. of ECE
 SKIT, Bengaluru



Event Report

Activity : Faculty development program on “Think about Thinking”

INTRODUCTION/OVERVIEW:

Event name: Faculty development program on “Think about Thinking”

Resource person: Prof. Manjunath S, Master Trainer, Accurise - Bengaluru.

Date of conducted: 12/04/2023

Number of participants: 30

Platform used: Seminar Hall

OBJECTIVES:

Designed the training programs to ignite minds of the audience and relearn from their experiences.

SUMMARY:

Climate setting by creating a friendly atmosphere, Stimulate interest and curiosity as well as Enable participants to begin thinking about the workshop.

To make the audience understand the Importance of Goals is to make the audience set their goals and Develop holistic goals.

Make the participants to understand the much needed approaches in teaching for Procedure & Instruction of the activity as well as debriefing the activity

OUTCOMES:

1. The participants can understand Types of Thinking, Problem of the Day, Industry Expectation, Ice breakers and the needed approaches in teaching.
2. This Faculty induction program helped them to ignite minds of the audience and relearn from their experiences.
3. This Faculty induction program helped Setting expectations /Objectives for the day.



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SCREENSHOTS/PICTURES:





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Flyer:

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Department of Electronics & Communication Engineering
In association with

INTERNAL QUALITY ASSURANCE CELL
ORGANIZES
FACULTY DEVELOPMENT PROGRAM

THINK ABOUT THINKING
ON
12th APRIL 2023

TIME: 1:30 PM ONWARDS
VENUE: SKIT AUDITORIUM

RESOURCE PERSON	EVENT CO-ORDINATORS
Mr. Manjunatha. S Master Trainer & Life Coach	Dr. J. Divya Lakshmi Member, IQAC Prof. Shwetha. R Asst. prof. Dept of ECE

Dr. N.C. Patil **Dr. Savita B Hosur** **Dr. Mahesha K**
HOD, ECE Head, IQAC Principal, SKIT

<http://www.skit.org.in>

Address: No. 29, Chimney Hills, Hesaraghatta Road, Chikkabanavara Post,
Bangalore- 560 090

Event Coordinator/s



Date:27/05/2023

Event Report

Activity :”A Webinar on Intellectual Property Rights and Patenting”

INTRODUCTION/OVERVIEW:

Department of Electronics and Communication engineering had organized a Webinar on Intellectual Property Rights and Patenting on Saturday 27/5/2023. It was Co-ordinated by Mrs.Kiranmayi, Asst. prof. Dept. of ECE

The event was conducted in online mode at 10:30AM and approximately 60 participants had attended the event The participants being heads of various departments and faculties.

A very renowned resource person Dr K.K. Baseer, Professor of IT who is an Innovation ambassador for IPR and Teccnology in SVCE college , Tirupathi , presented on the topic IPR and Patenting.

OBJECTIVES:

1. To Introduce fundamental aspects of intellectual property Rights
2. To disseminate knowledge on patents, patent regime in India and abroad and registration aspects.
3. To disseminate knowledge on copyrights and its related rights and registration aspects .
4. To disseminate knowledge on trademarks and registration aspects.
5. To become aware of current trends in IPR and Govt. steps in fostering IPR.

SUMMARY:

Department of ECE organized a webinar on Intellectual property rights and Patenting in order to create awareness on Copyrights, trademarks and rights related to it. Also it was brought to the knowledge of the participants how patents , trademarks, geographical indications, industrial design play an important role in the modern economy. The event ended with a questionnaire session and a note to conduct a hands on session on the same topic in offline mode in further days to come

OUTCOMES:

1. The participants will able understand the fundamental aspects of Intellectual Property rights
2. Understand the concept of rights , trademarks, current trends in IPR and registration.



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INTERNAL QUALITY ASSURANCE CELL (IQAC)

Flyer

The flyer is a vertical rectangular graphic with a dark blue background and teal accents. At the top left is the Sri Krishna Institute of Technology logo. To its right, text reads: 'Sri Krishna Educational Institutions Society (S) Sri Krishna Institute of Technology Approved by AICTE, Accredited by IQAC, Affiliated to SVCE'. Further right are the AICTE and IQAC logos. The main text in the center reads: 'Department of Electronics & Communication Engineering Organizes Webinar on Intellectual Property & Patenting'. Below this, it says 'SATURDAY 27 MAY 2023' and 'ONLINE at 10:30AM'. A small portrait of Dr. K. K. Baseer is shown. To the right of the portrait, his name and title are listed: 'Dr K K Baseer Professor of IT SVCE-EC Innovation Ambassador for IPR and Technology Sree Vidyanikethan Engineering College, Tirupathi'. At the bottom left, there are two sections: 'Registration Link' with the URL 'https://forms.gle/n8d2NYLuZ7ib1BXb9' and 'Meet Link' with the URL 'https://meet.google.com/pjr-ibfq-mch', phone number '401-592-7156', and PIN '713 366 917#'. At the very bottom left, there is a teal arrow pointing right and the website 'www.skit.org.in'.

Kiranmayi M
Event Coordinator/s



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Internal Quality Assurance Cell (IQAC)

Ref no: SKIT/IQAC/2022-23/17

Date: 11.05.2023

CIRCULAR

The Internal Quality Assurance Cell (IQAC) invites all the Departments Instructors to attend a workshop on "Empowering Non-Teaching Staff: A Workshop on System Troubleshooting", which will be held on 17-05-2023 at 2:00 PM. The session will take place at Skill Development Lab, B Block, 1st Floor.

This hands-on training program is designed to provide non-teaching staff with the skills and knowledge they need to troubleshoot common computer system issues. The workshop will cover the basics of computer systems and troubleshooting techniques, and participants will have the opportunity to practice diagnosing and resolving common issues on their own. We will also focus on promoting a culture of self-sufficiency and collaboration within the organization, by encouraging participants to share their knowledge and skills with others.

Resource Person: Mr. Prasad, System Admin, SKIT.

Event Coordinator: Mr. Imran Ulla Khan, Member IQAC, Dept. of CSE, SKIT

Savita
Head, IQAC 11/5/23
Dr. Savita B. Hosur

Dr. Mahesha K.
Principal 11 May 23
Dr. Mahesha K.

AI&ML	CSE	ISE	ECE	ME	CY	BS	IQAC
<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	

- Copy to: 1. All Departments
2. IQAC Members
3. Administrative Office



Date: 17-05-2023

Event Report

Activity: "Empowering Non-Teaching Staff: A Workshop on System Troubleshooting"

INTRODUCTION/OVERVIEW:

The Internal Quality Assurance Cell (IQAC) has organized an "Empowering Non-Teaching Staff: A Workshop on System Troubleshooting" on 17-05-2023 at 2:00 PM, Skill Development Lab, B block 1st Floor. For all the departments instructors of SKIT.

Session started with Prof. Imran Ulla Khan, Member IQAC welcomed all the participants and the resource person Mr. Prasad, System Admin, SKIT.

This hands-on training program is designed to provide non-teaching staff with the skills and knowledge they need to troubleshoot common computer system issues. The workshop will cover the basics of computer systems and troubleshooting techniques, and participants will have the opportunity to practice diagnosing and resolving common issues on their own. We will also focus on promoting a culture of self-sufficiency and collaboration within the organization, by encouraging participants to share their knowledge and skills with others.

The resource person Mr. Prasad explained the common technical issues raised during the operation of computer system and demonstrated how to resolve them. He also demonstrated how to install operating system through CD and pen drive.

Prof. Imran Ulla Khan whole-heartedly thank Management, Principal and Head IQAC for their support. Thanks to Resource person for sharing his knowledge and expertise with participants. Finally thanks to all participants who took part in this session.

OBJECTIVES:

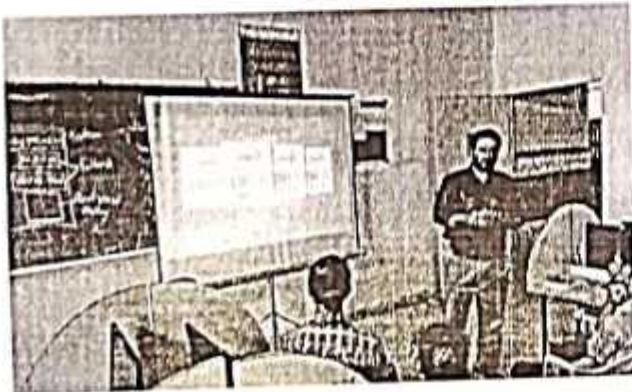
1. To provide non-teaching staff with a basic understanding of computer systems and troubleshooting techniques, so that they can diagnose and resolve common issues on their own.
2. To increase the confidence and competence of non-teaching staff in dealing with technical problems, so that they feel more empowered to take ownership of their work and support others.
3. To promote a culture of self-sufficiency and collaboration within the organization, by encouraging non-teaching staff to share their knowledge and skills with others.



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SCREENSHOTS/PICTURES:



Imran Ulla Khan
Event Coordinator
Prof. Imran Ulla Khan
Member, IQAC
Dept. CSE, SKIT



Date: 17-05-2023

Empowering Non-Teaching Staff: A Workshop on System Troubleshooting

Attendance sheet

SL#	Name	Department	Signature
1.	Mrs. Pankajakshi	CSE	Pankajakshi B.R.
2.	Mrs. Radha G H	ECE	Radha G.H.
3.	Mr. Pradeep V	— Absent —	
4.	Mrs. Umadevi	CSE	Umadevi
5.	Mrs. Nandini G R	CSE	Nandini G.R.
6.	Ms. Vedha U	CIVIL	Vedha U.
7.	Mr. Upendra B A	CIVIL	Upendra B.A.
8.	Mr. Kiran kumar G	— Absent —	
9.	Mr. Krishna Prasad	— Absent —	
10.	Ms. Janavi M	ISE	Janavi M.
11.	Mrs. Sujatha H V	ISE	Sujatha H.V.
12.	Mrs. Prithvi S	ISE	Prithvi S.
13.	Mrs. Deepika S U	BS	Deepika S.U.
14.	Mr. Ravi Kumar P	B.S	Ravi Kumar P.
15.	Mrs. Usha Rani	— Absent —	
16.	Ms. Rekha G R	AIML	Rekha G.R.
17.	Mrs. Kavya M N	ECE	Kavya M.N.



Date: 03-04-2023

Orientation session on "CO PO Attainment calculation"

Attendance sheet

SL#	Name	Department	Signature
1.	Prof. Sowmya CV	CSE	
2.	Prof. Aruna R	CSE	
3.	Prof. Latha A	CSE	
4.	Prof. Imran Ulla Khan	CSE	
5.	Prof. Veena M Naik	ISE	
6.	Prof. Ragini Krishna	ISE	
7.	Prof. Nayana	ISE	
8.	Prof. Ramya	Civil	
9.	Prof. Chaitra A S	ECE	
10.	Prof. Kiranmayi	ECE	
11.	Prof. Shilpa V	ECE	
12.	Prof. Namratha D	ECE	
13.	Prof. Soujanya G	AIML	
14.	Prof. Veerabhadra Swamy	BS	
15.	Prof. Shwetha	BS	
16.	Prof. Appese D	ME	



Date: 03-04-2023

Event Report

Activity: Orientation Session on “CO PO Attainment calculation”

INTRODUCTION/OVERVIEW:

The Internal Quality Assurance Cell (IQAC) has organized an Orientation session on “CO PO Attainment calculation” on 03-04-2023 at 10:00AM, Seminar Hall, A block 1st Floor. For all the departments CO-PO Attainment coordinators and the newly joined faculty (those who have not undergone previous training) of SKIT.

Session started with Prof. Imran Ulla Khan, Member IQAC highlighted the importance of CO PO Attainment calculations and welcomed all the participants and the resource person Prof. Kiranmayi, CO PO Attainment coordinator of the college

The resource person explained importance of course outcomes and how to frame course outcomes along with how to assess students' learning outcomes and how to track their progress towards achieving the program outcomes. And she also demonstrated how to calculate the attainment with some sample data.

The participants interacted well with resource persons. Total 16 faculties (Departments CO PO Attainment coordinators and newly joined faculties) have actively participated and drawn benefit from this session.

Prof. Imran Ulla Khan whole-heartedly thank Management, Principal and Head IQAC for their support. Thanks to Resource person for sharing her knowledge and expertise with participants. Finally thanks to all participants who took part in this session.

OBJECTIVES:

- Clarify the meaning of Course Outcomes (COs) and Program Outcomes (POs)
- Explain the importance of COs and POs
- Demonstrate how to assess COs and POs
- Discuss ways to improve COs and POs

SUMMARY:

A Course Outcomes (COs) and Program Outcomes (POs) attainment orientation session is designed to provide faculty members with a better understanding of COs and POs and their importance in measuring student learning and program effectiveness. The session will equip participants with the knowledge and skills necessary to assess COs and POs using various assessment techniques and methods, and to align their teaching practices with COs and POs to improve student learning



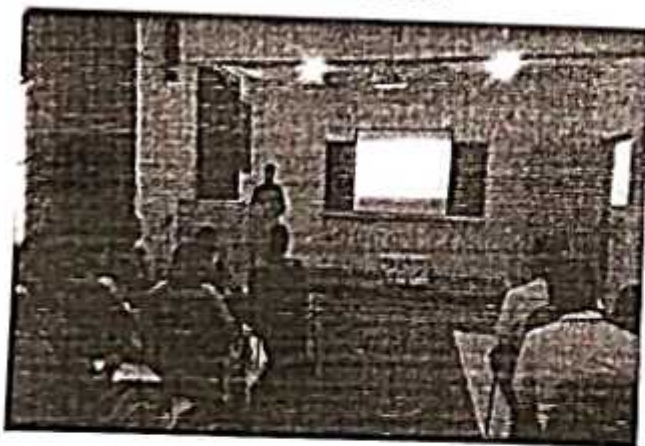
outcomes. The session will also provide a forum for discussing ways to improve COs and POs and promote ongoing program development. Overall, the goal of the session is to improve the quality of teaching and learning by promoting faculty engagement and improving student outcomes and program effectiveness.

OUTCOMES:

- Improved understanding of COs and POs: Participants will have a better understanding of what COs and POs are, how they differ from each other, and their importance in measuring student learning and program effectiveness.
- Increased ability to assess COs and POs: Participants will be equipped with knowledge and skills to effectively evaluate COs and POs using various assessment techniques and methods.
- Enhanced ability to align teaching with COs and POs: Participants will have a better understanding of how to align their teaching practices with COs and POs to improve student learning outcomes.
- Improved accreditation outcomes: Participants will be better prepared to demonstrate compliance with accreditation requirements related to COs and POs.

Overall, the outcome of a COs and POs attainment orientation session is to improve the quality of teaching and learning by providing faculty members with the knowledge and skills necessary to effectively assess and align their teaching practices with COs and POs, ultimately leading to improved student outcomes and program effectiveness.

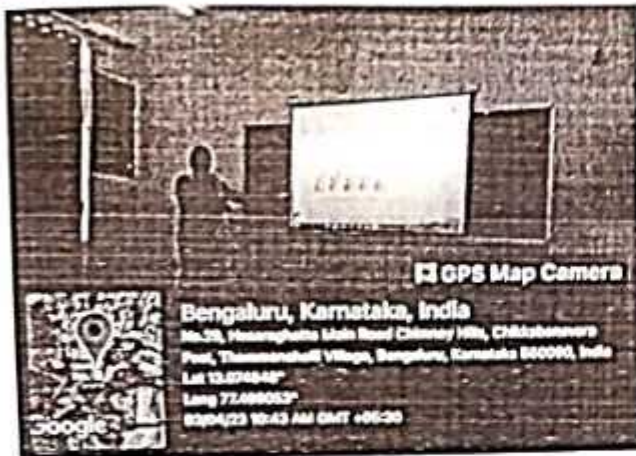
SCREENSHOTS/PICTURES:






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#29, Chimney Hills, Hexaraghatta Main Road, Chikkabanaswara Post, Bengaluru - 560090




Event Coordinator 3/4/23
Prof. Imran Ulla Khan
Member, IQAC
Dept. CSE, SKIT

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Date: 09/03/2023

Event Report : Faculty Induction Program

INTRODUCTION/OVERVIEW:

Event name - Faculty Induction Program

Date of conduction - 09/03/2023

Number of participants - 12

platform used - Offline

Venue - Seminar hall , A block

Speaker - Ms.Archana

OBJECTIVES: To help the participants to understand the structure , functioning, governance ,rules and regulations in the institutions of technical education and to orient them to become potential partners in the institute building.

SUMMARY : The faculty induction program threw lights on the areas such as our organisation structure and it's leadership teams, core values, SKIT vision and mission, functions of IQAC, SKIT on the path of progress, professional ethics rules and responsibility of the faculty for conducting and participating in webinars and seminars. As a proctor mentor maintaining documents and files invigilation VTU documentation registration work active participation in IQAC, NBA & NAAC documentation process paper publication and research, supporting student admission also it tells about academic leadership, communication skills, do and don'ts of SKIT, various policies of SKIT etc.

OUTCOMES:

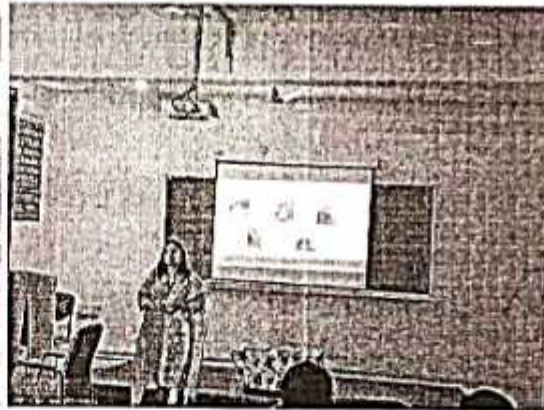
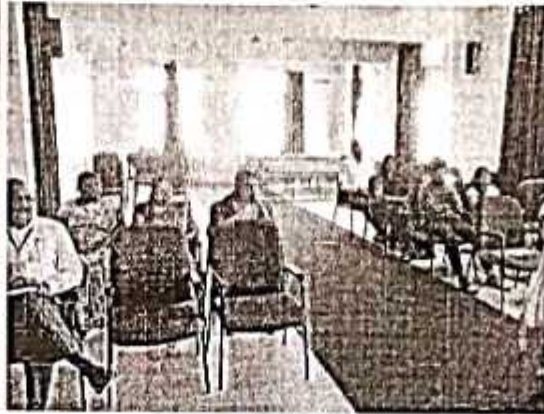
The orientation programme familiarized and assisted the new employees with their employer workforce and job design. The FIP was able to improve the self confidence of the faculty members and motivated them to prepare an action plan in there work space.



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Event Coordinators

Mr. Veerabhadraswamy C (Dept. of Maths)


Head of Department



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Internal Quality Assurance Cell (IQAC)

Event Conduction Approval

**Application must be submitted two weeks prior to the date of event*

Event Coordinator name: Mrs. Sowmya C V

Email: sowmyacvcese@skit.org.in

Contact no: 7406750518

Department: Computer Science and Engineering

Event name: Workshop on Machine Learning

Resource Person's: Ms. Megha

Date of conduction of the event: 07/10/2022

Type of Event (Technical/Extracurricular/Others (be specific)): Technical

Event Description:

The main objective of this course is to enabling the student with basic knowledge on the techniques to build an intellectual machine for making decisions behalf of humans. This course covers the techniques on how to make learning by a model, how it can be evaluated, what are all different algorithms to construct a learning model.

As we know machine learning can be considered as a most trending technology in present and as well as in future it's very important to get exposure.

From Mevi technologies LLP we would like conduct a hands-on machine learning workshop for a day (7-8 hours).

We start from basics and end the workshop with one project where they might take that project to advance level like final year project.

Outcome of the Event or Activity:

Upon completion of this workshop the student is able to:

- Develop an appreciation for what is involved in Learning models from data.
- Understand a wide variety of learning algorithms.
- Understand how to evaluate models generated from data.
- Apply the algorithms to a real problem. optimize the models learned and report on the expected accuracy that can be achieved by applying the models.



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Workshop Breakdown

Session Description:

- Introduction to python data structures (45 min)
- scikit-learn
- numpy
- pandas
- intro to machine learning
- types of machine learning
- exploring the data and visualizing the data using matplotlib and seaborn
- types of data and dataset
- training and testing data
- confusion matrix
- introduction to machine learning algorithms
- linear regression and logistic regression
- 2+1 projects
- Intro to open cv(bonus lecture)

Requirements:

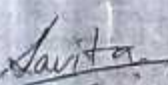
Laptop & internet as it will be a hands-on session software requirements - anaconda with python 3.x e- certificate will be provided.

Estimated attendance: 80 students from CSE and ISE

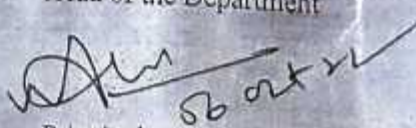
Venue of the event: CSE ICT Class Room

Estimated cost for the event: 4000


Event Coordinator 06/10/22


IQAC Head 06/10/22


Head of the Department 06/10/2022


Principal 06/10/22



Event Report

Activity : Workshop on Machine Learning

INTRODUCTION/OVERVIEW:

{Event name: Workshop on Machine Learning, Resource person: Ms. Megha B S, Date of conduction: 07/10/2022, Number of participants: 74 students from CSE and ISE and Platform used – CSE ICT classroom }

Event Description:

Machine Learning is one of the most popular sub-fields of Artificial Intelligence. Machine learning concepts are used almost everywhere, such as Healthcare, Finance, Infrastructure, Marketing, Self-driving cars, recommendation systems, chatbots, social sites, gaming, cyber security, and many more. Currently, Machine Learning is under the development phase, and many new technologies are continuously being added to Machine Learning. It helps us in many ways, such as analyzing large chunks of data, data extractions, interpretations, etc. Hence, there are unlimited numbers of uses of Machine Learning.

The machine learning field is continuously evolving. And along with evolution comes a rise in demand and importance. There is one crucial reason why data scientists need machine learning, and that is: 'High-value predictions that can guide better decisions and smart actions in real-time without human intervention.

As we know machine learning can be considered as a most trending technology in present and as well as in future it's very important to get exposure.

From Mevi technologies LLP we would like conduct a hands-on machine learning workshop for a day (7-8 hours).

We start from basics and end the workshop with one project where they might take that project to advance level like final year project.

Prerequisites:

Machine Learning Crash Course does not presume or require any prior knowledge in machine learning. However, to understand the concepts presented and complete the exercises, we recommend that students meet the following prerequisites:



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- Students must be comfortable with variables, linear equations, graphs of functions, histograms, and statistical means.
- Students should be a good programmer. Ideally, you should have some experience programming in Python because the programming exercises are in Python. However, experienced programmers without Python experience can usually complete the programming exercises anyway.

OBJECTIVES:

The objective of the workshop is:

- To understand the basic theory underlying machine learning.
- To be able to formulate machine learning problems corresponding to different applications.
- To understand a range of machine learning algorithms along with their strengths and weaknesses. To be able to apply machine learning algorithms to solve problems of moderate complexity.
- To apply the algorithms to a real-world problem, optimize the models learned and report on the expected accuracy that can be achieved by applying the models.

SUMMARY:

The main aim of this workshop is to enabling the student with basic knowledge on the techniques to build an intellectual machine for making decisions behalf of humans. This course covers the techniques on how to make learning by a model, how it can be evaluated, what are all different algorithms to construct a learning model.

OUTCOMES:

Upon completion of this workshop the student is able to:

- Develop an appreciation for what is involved in Learning models from data.
- Understand a wide variety of learning algorithms.
- Understand how to evaluate models generated from data.
- Apply the algorithms to a real problem, optimize the models learned and report on the expected accuracy that can be achieved by applying the models.

Topics covered in Workshop:



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Introduction to Programming, Python basics, Applications of python, data types, string methods, loops (for and while), conditional statements, 3 to 4 programs in python.

Introduction to machine learning, Types of machine learning, different algorithm in machine learning, data set, Training and testing data sets libraries, numpy and pandas, what are real world applications of machine learning, 1 algorithm linear regression Salary prediction of employee mini project.

SCREENSHOTS/PICTURES:



Flyer:



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Sri Raghavendra Educational Institutions Society
Sri Krishna Institute of Technology
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WORKSHOP ON

MACHINE LEARNING



RESOURCE PERSON

**MS.
MEGHA B S**

COMPANY: *MeVi Technologies* DESIGNATION: *Trainer and Developer*

OBJECTIVES:

- Develop an appreciation for what is involved in Learning models from data.
- Understand a wide variety of learning algorithms.
- Understand how to evaluate models generated from data.
- Apply the algorithms to a real problem, optimize the models.

DATE: 07/10/22

TIME: 9:00AM-4:00PM

VENUE: CSE ICT ROOM

CO-ORDINATOR: SOWMYA C.V

PH NO: 7406750518

Certificate:

Sowmya C.V.
Event Coordinator/s
Sowmya C V